Hogan & Hartson 81751.0066 Hiroshi MASUYA Semiconductor Device and Method... EV 325 217 457 US 5 Drawing Sheets; Sheet 1 of 5

FIG. 1

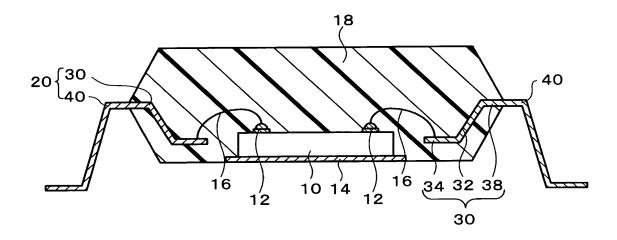
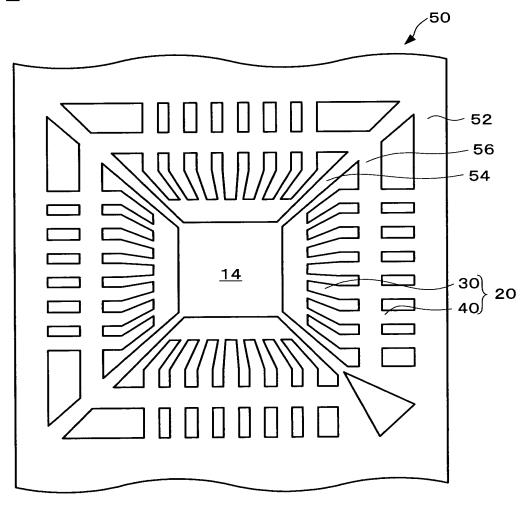


FIG. 2



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FIG. 3

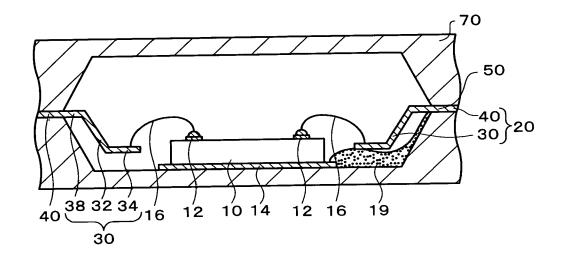
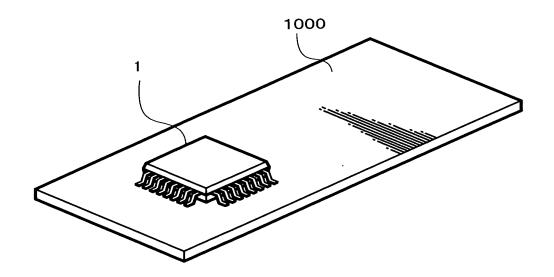


FIG. 4



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FIG. 5

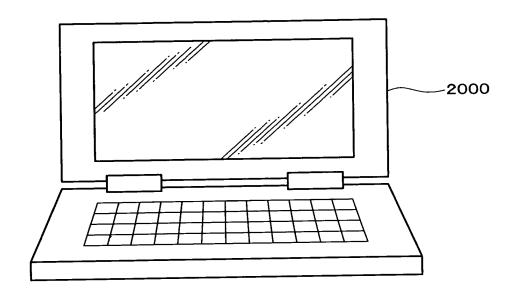
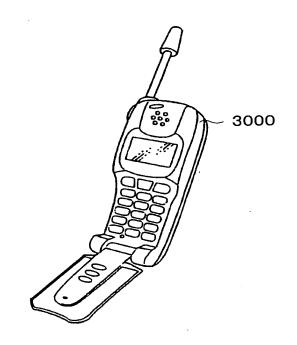


FIG. 6



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FIG. 7

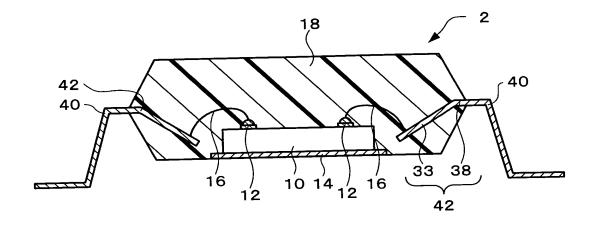


FIG. 8A

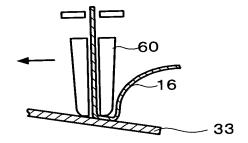
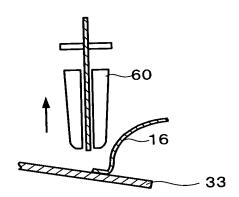


FIG. 8B



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FIG. 9

